

FIG. 1

FIG. 2

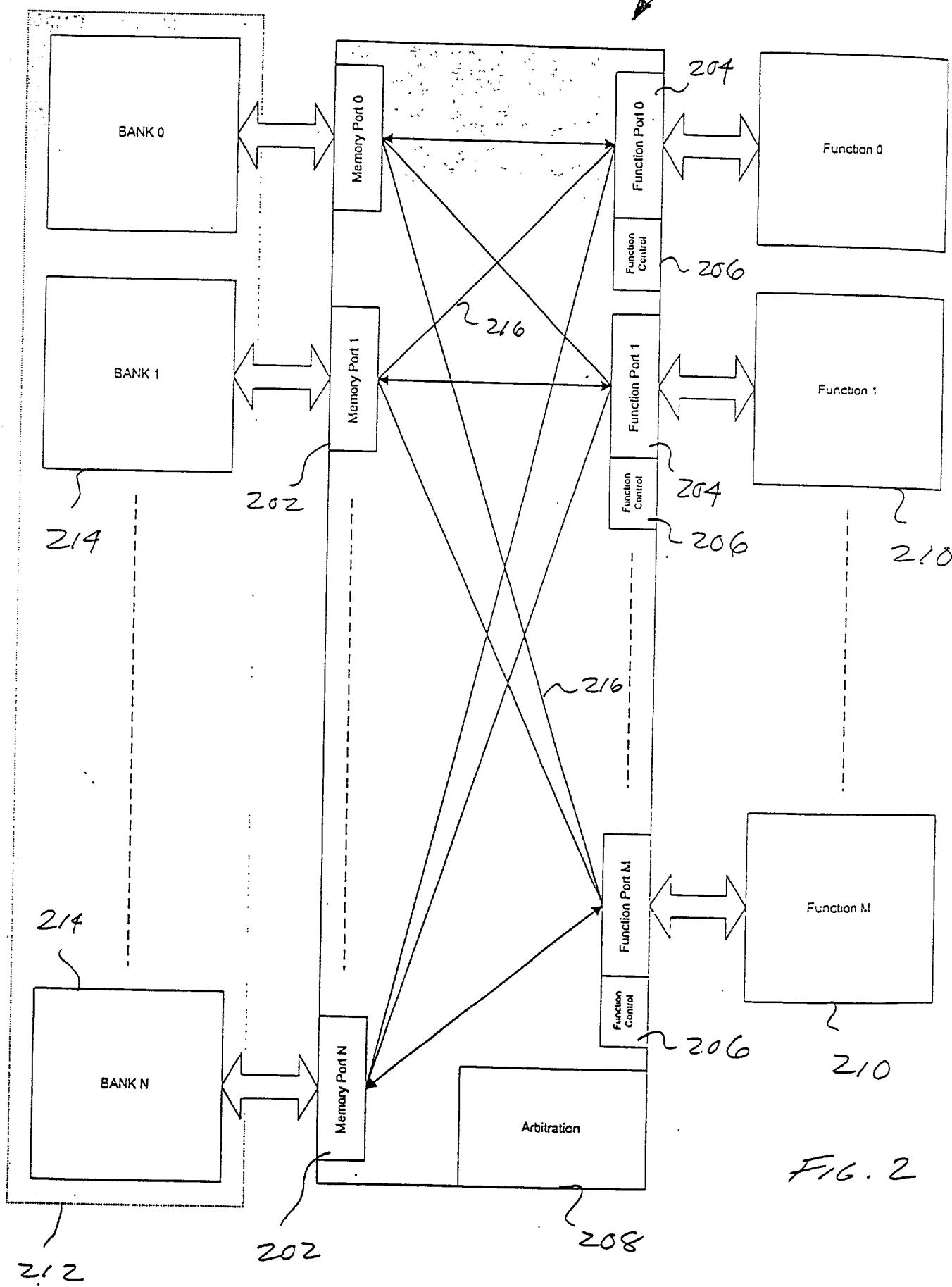


FIG. 2

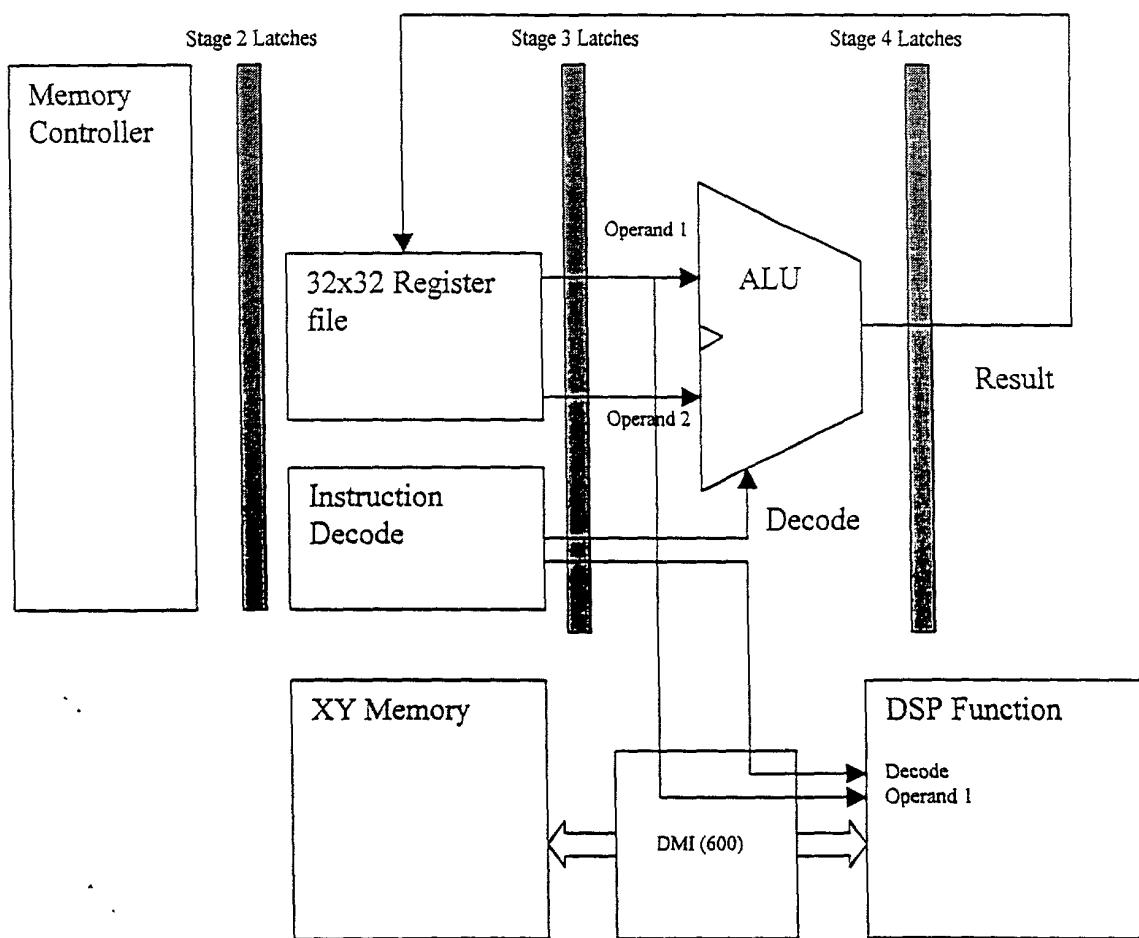


Fig. 2a

Fig. 3

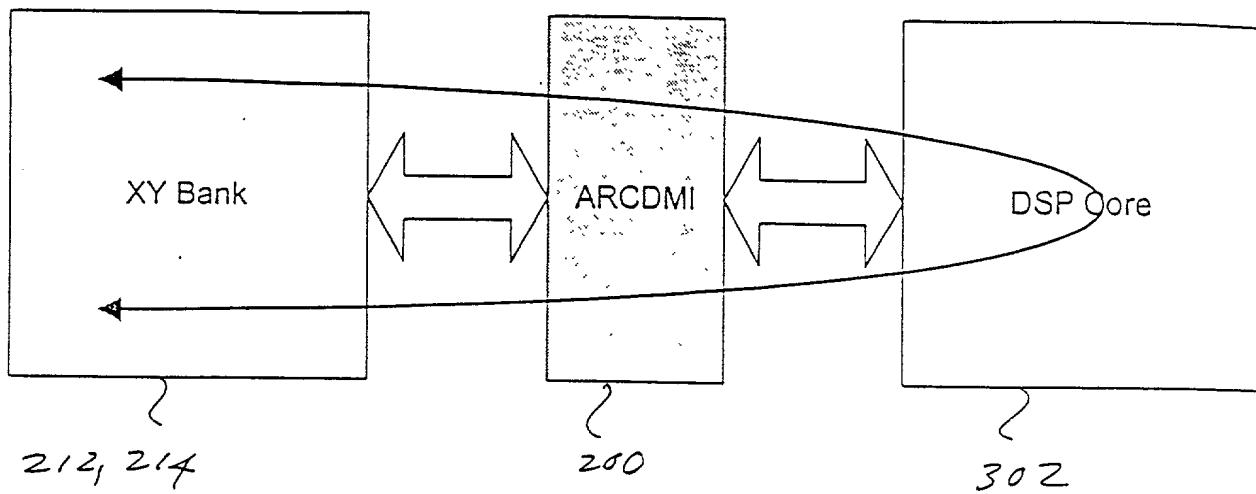


Fig. 5

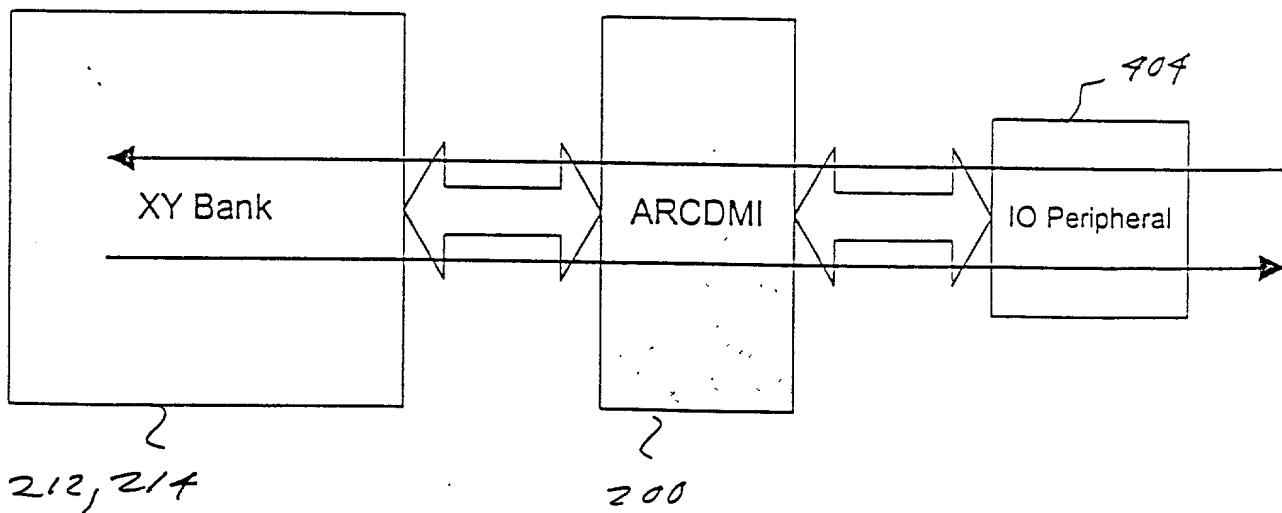
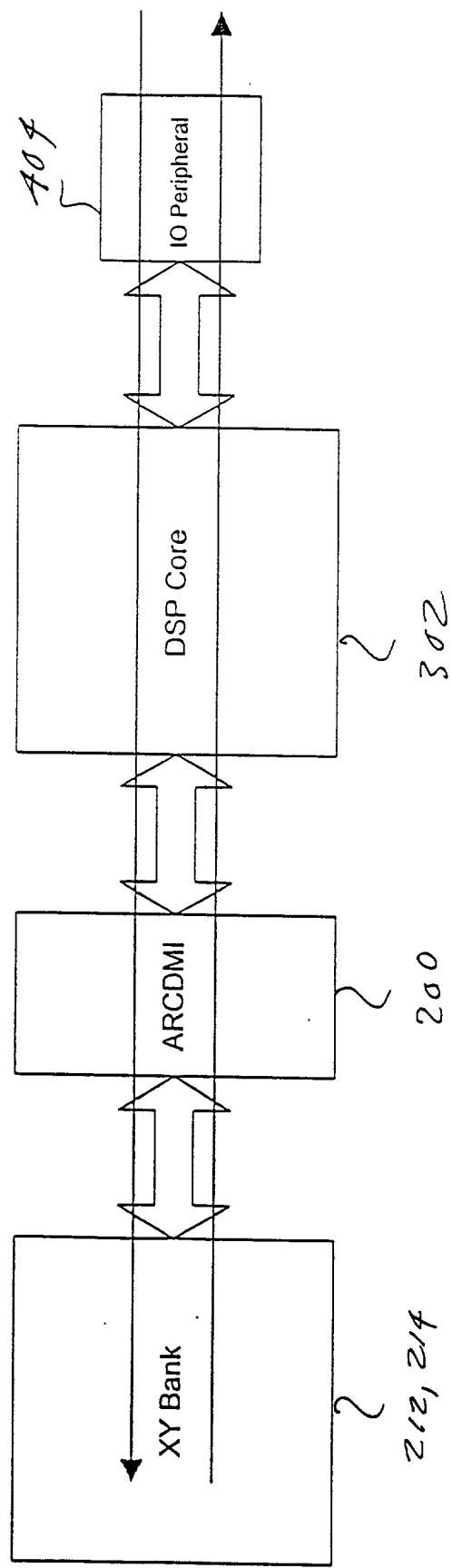
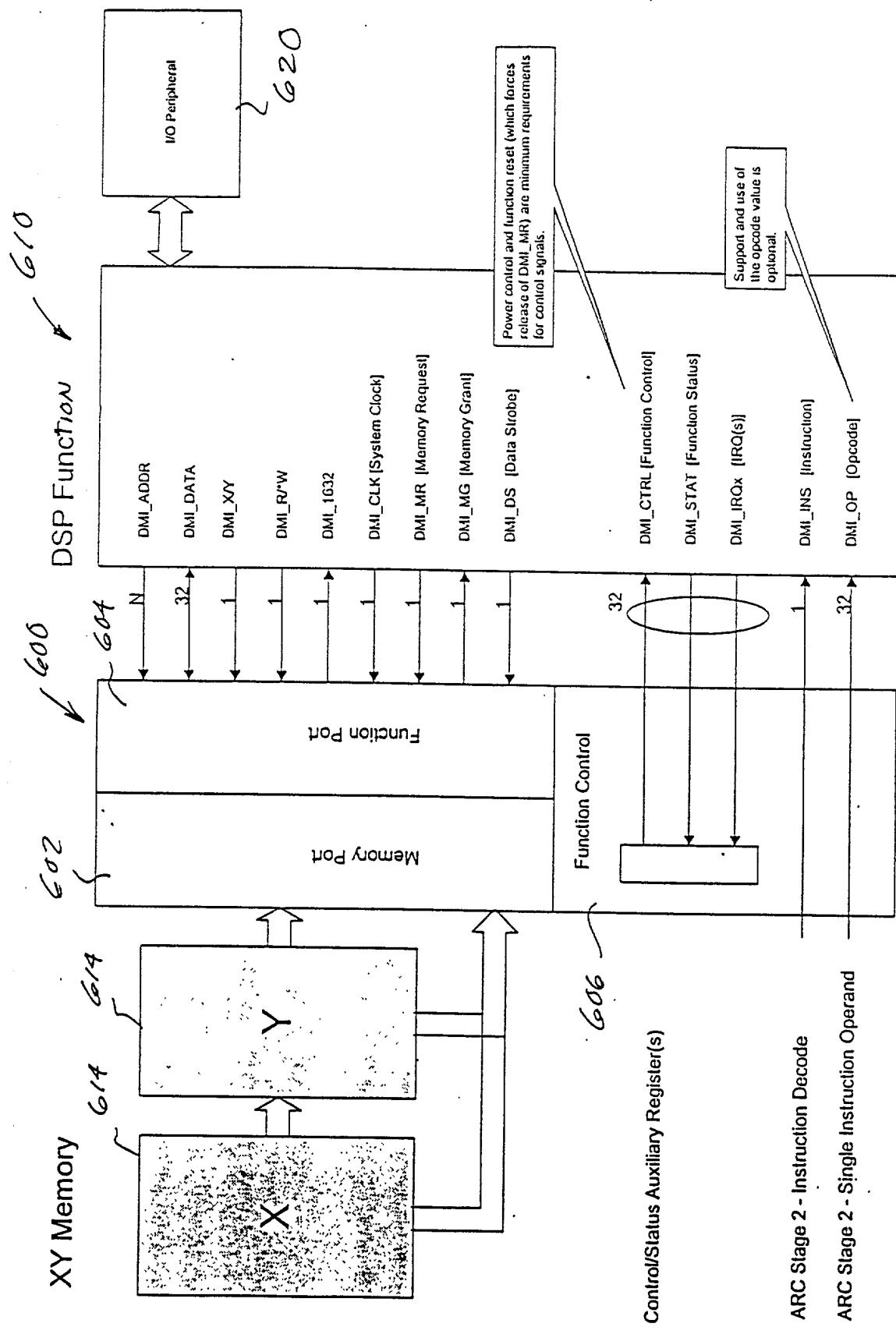


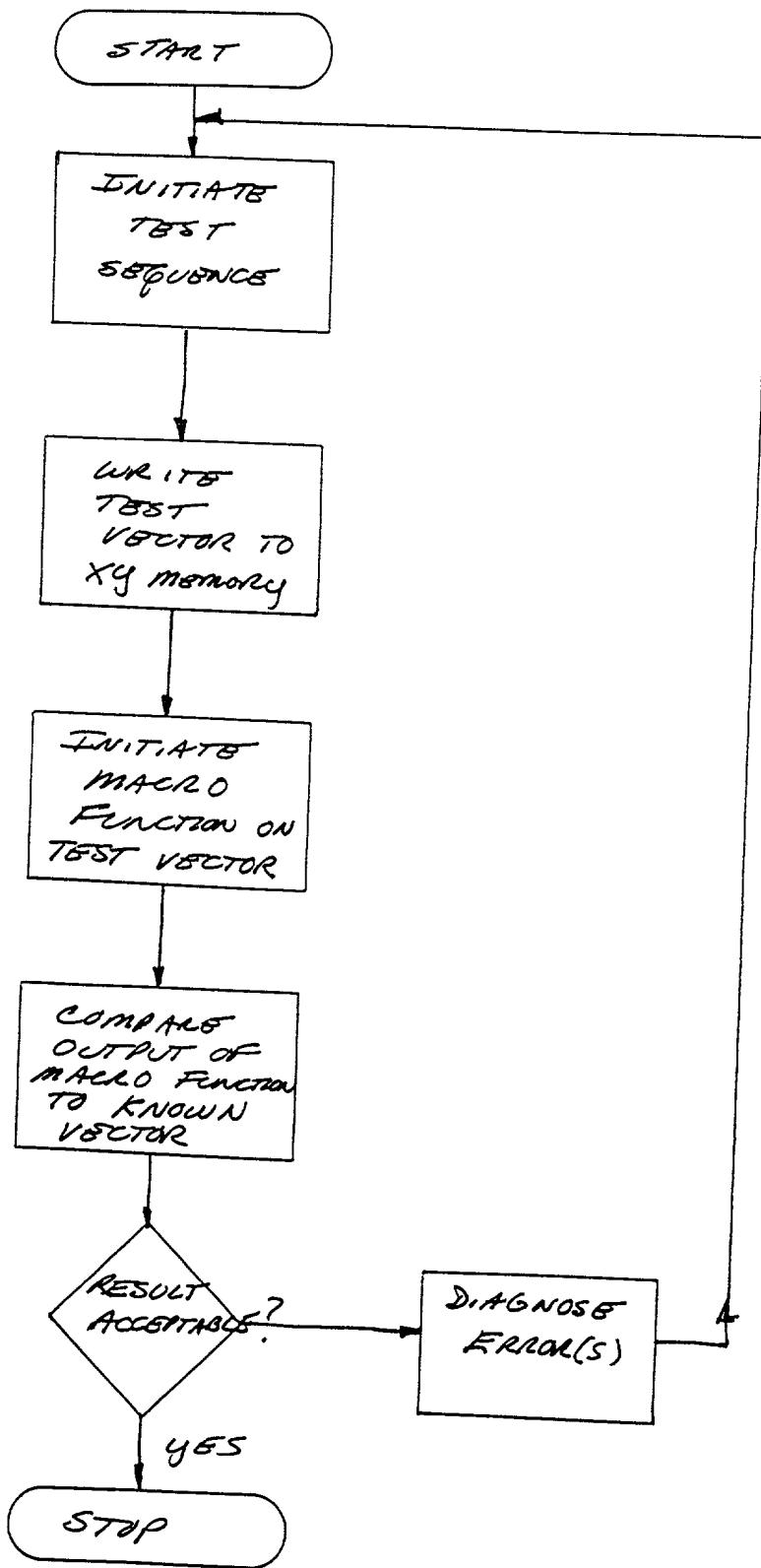
Fig. 4



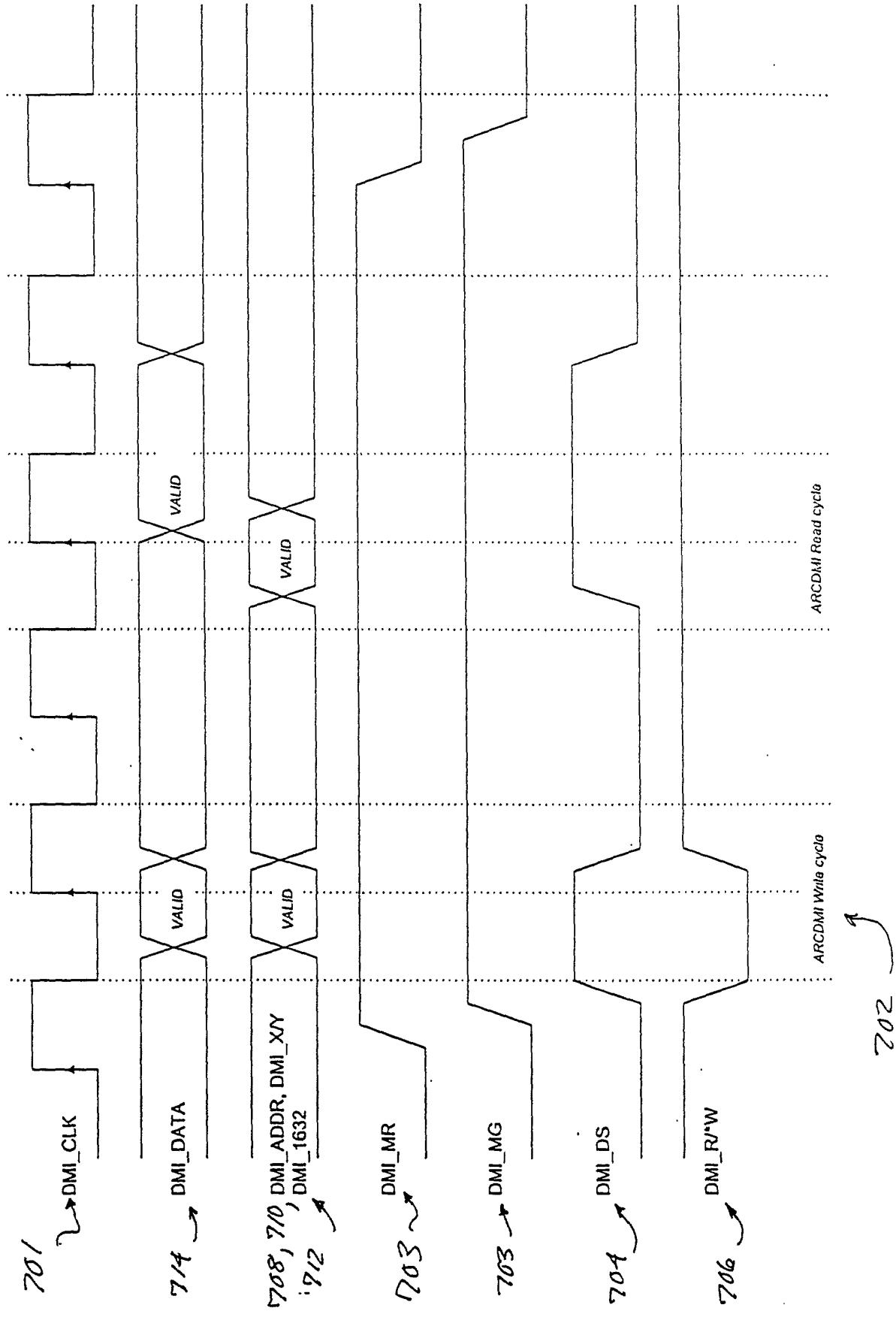


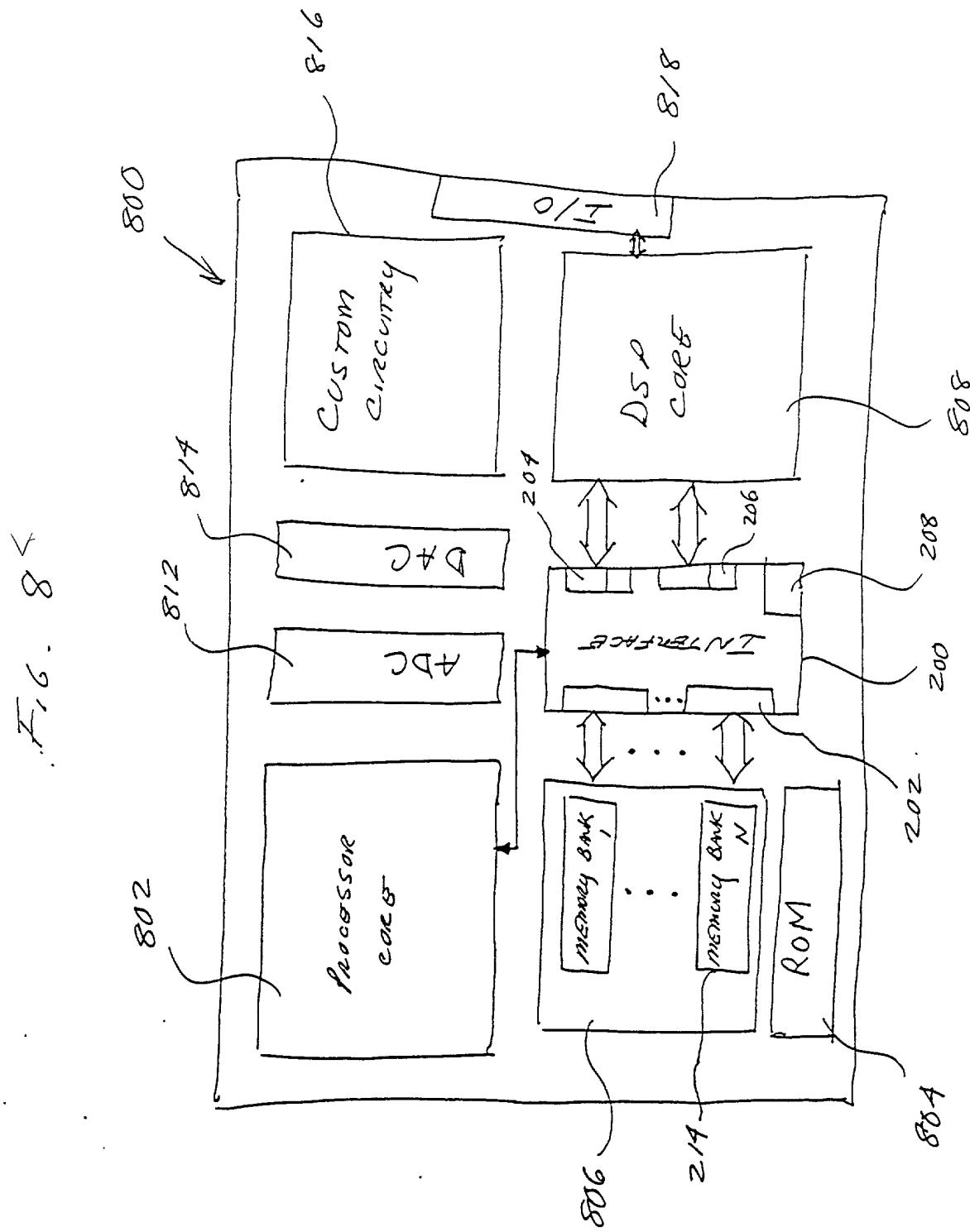
T. 6. 6

FIG. 6e



ARCDMI Read cycle





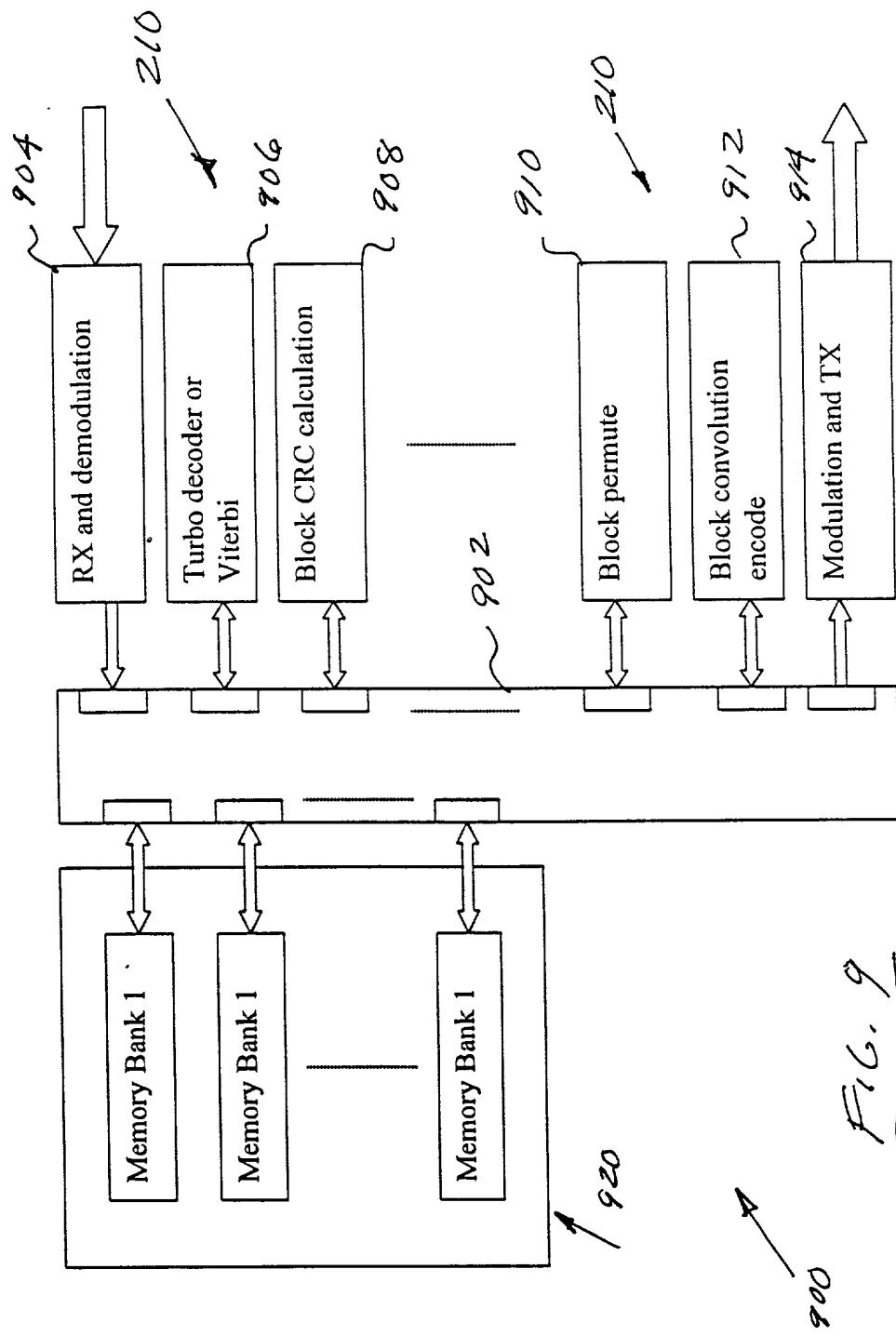
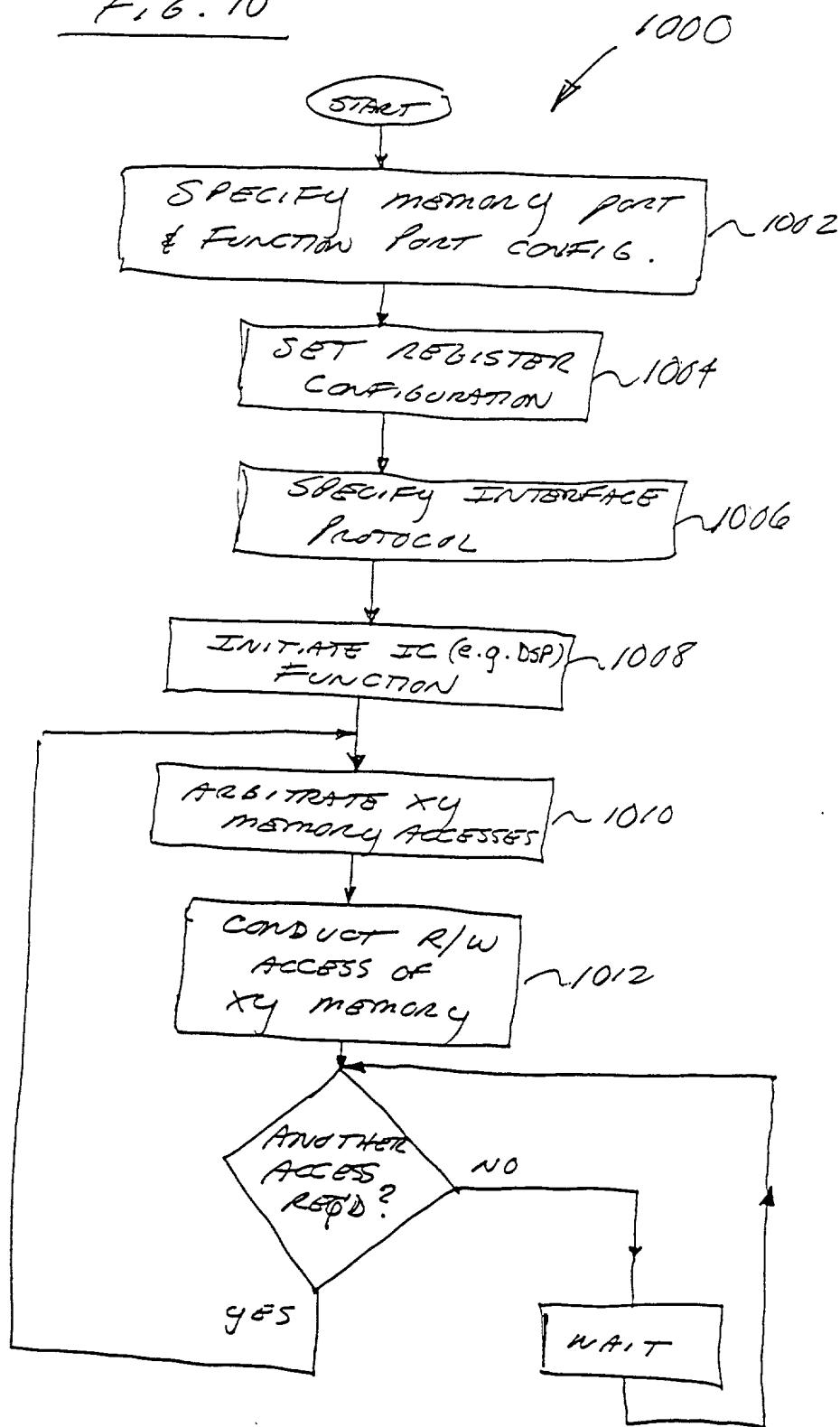


FIG. 10



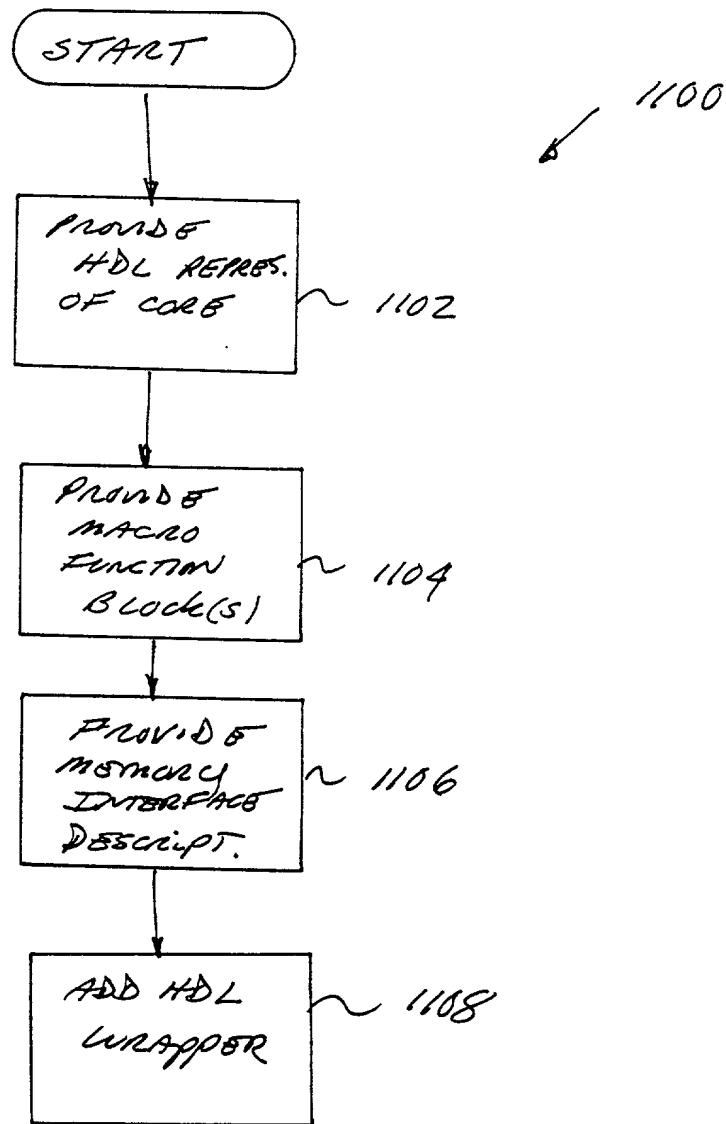


Fig. 11

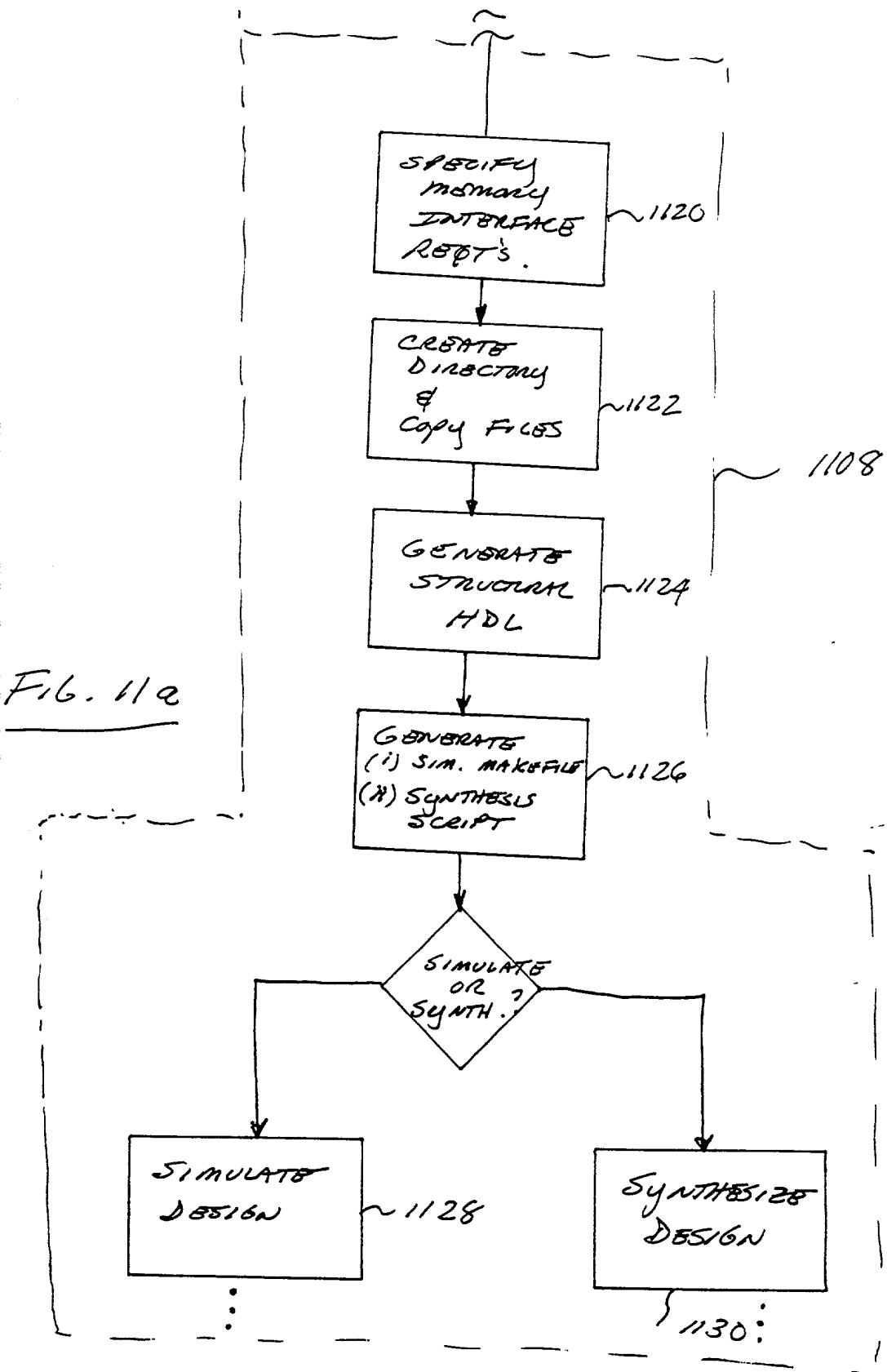


FIG. 12
(PART 1 OF 2)

